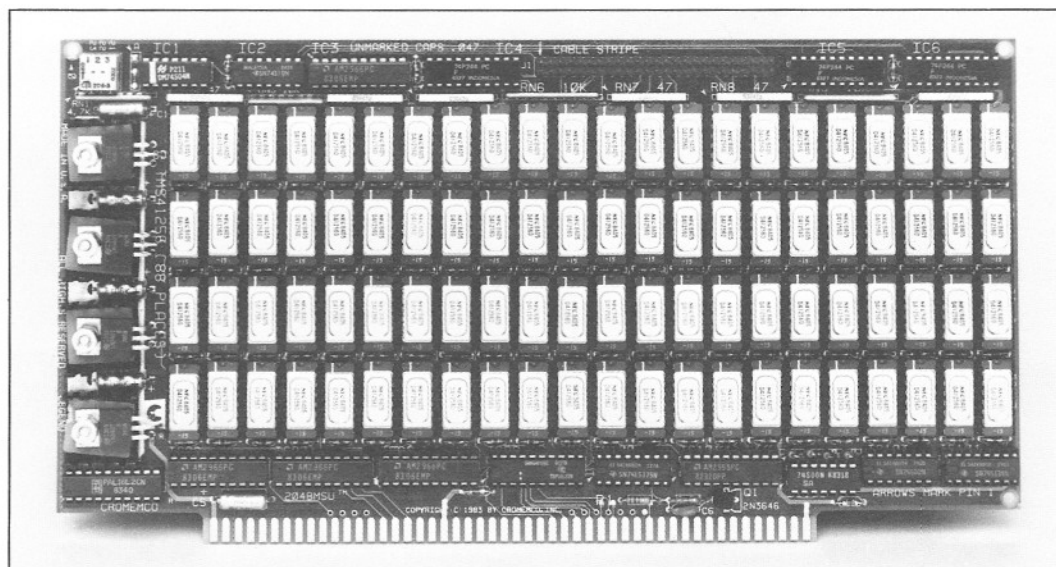


MODEL MCU and MODEL 2048MSU Memory Controller Unit and 2 Megabyte Memory Storage Unit

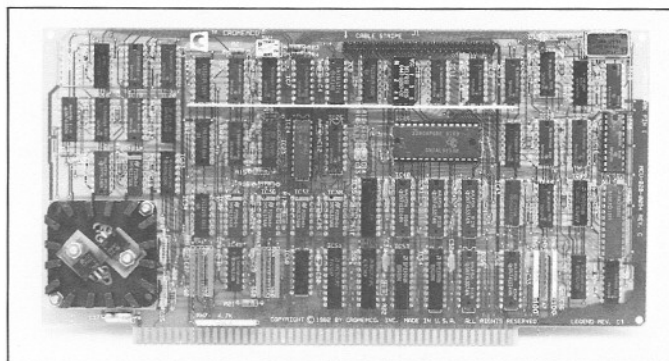


For use with the DPU, and XPU, Cromemco offers a 2 megabyte RAM memory unit. This unit works under the control of a Memory Controller Unit (MCU) which supports either byte or word-width operation.

The Memory Controller Unit can control up to six Memory Storage Units—a total of 12 megabytes of RAM using the 2048 kilobyte (2048MSU) unit.

A special feature of the memory unit is its built-in error checking and correction capability. In concert with this capability the Memory Controller Unit has an error-logging feature which stores the location of errors encountered and identifies the particular MSU that had the error. This feature is very helpful in system diagnostic and maintenance work.

For error checking and correcting, the memory unit is designed to use 22 bits to encode each 16-bit word. A modified Hamming code is also used which allows transparent detection and correction of single-bit errors and detection of double-bit errors.



TECHICAL SPECIFICATIONS

Model MCU

Support Capacity: Up to six MSU memory cards

Address Space: 16 Megabytes

Bus: S-100, (IEEE-696) compatible

Power Requirements: +8 volts @ 1.5A

±18 volts not used

Operating Environment: 0-55 degrees Centigrade

Model 2048MSU

Memory Capacity: 1024K by 22 bits: 2048 bytes

Memory Type: 256K dRAMs; 150 nanosecond access time

Bus: S-100, (IEEE-696) compatible

Power Requirements: +8 volts @ 1.5A

Operating Environment: 0 to 55 degrees Centigrade

Cromemco

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